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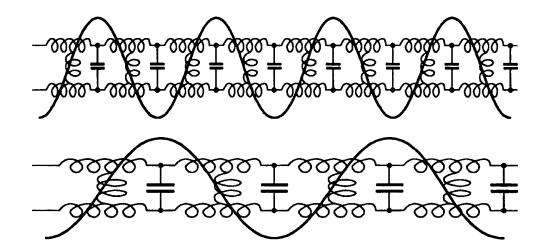
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FIG.1



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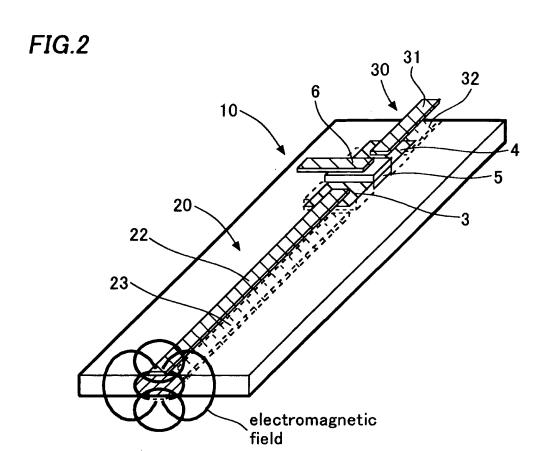
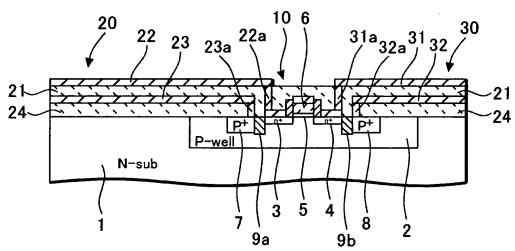


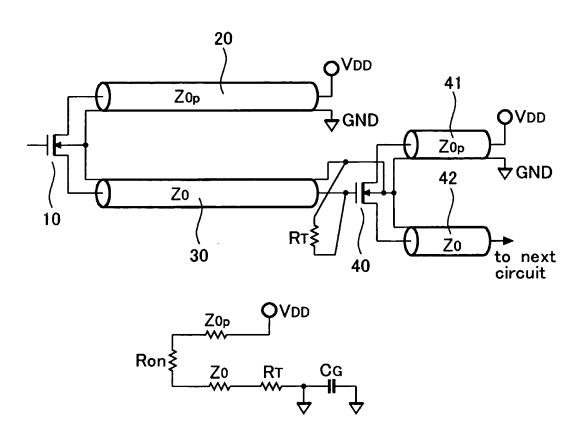
FIG.3



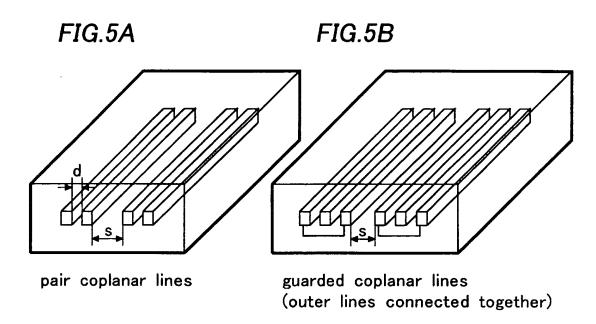
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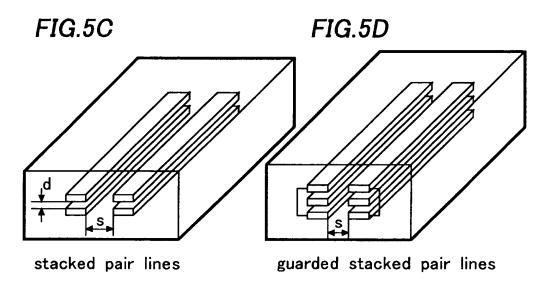
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FIG.4



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FIG.6

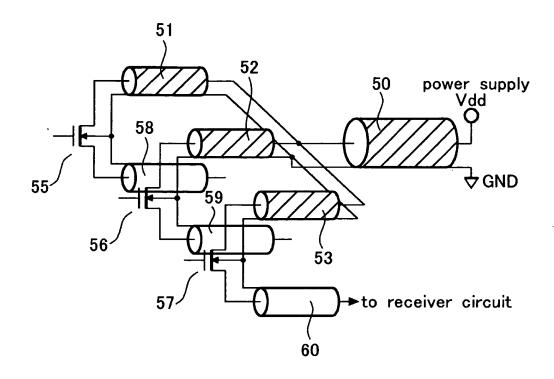
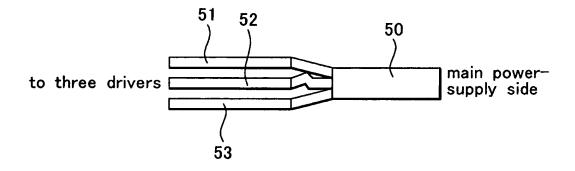


FIG.7



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FIG.8A

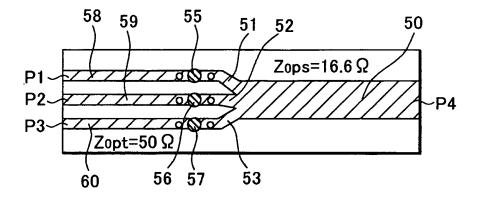
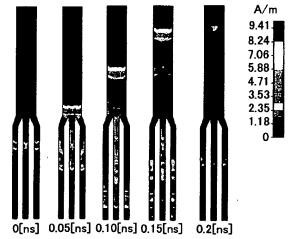


FIG.8B



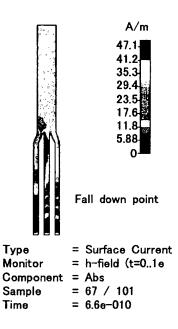
Туре = Surface Monitor = h-field Component = Abs Sample = 84 / 101 Time = 8.3e-010

simulation of progress of electromagnetic wave around confluence of power-supply/ground pair transmission lines

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FIG.9



simulation of progress electromagnetic when two of three driver transistors turn on ahead App No.: Not Yet Assigned Docket No.: 492322017000

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FIG.10A

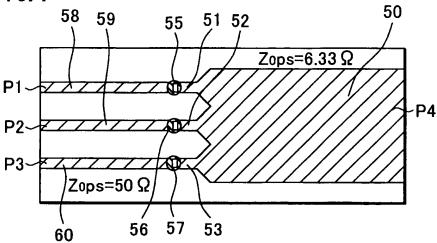
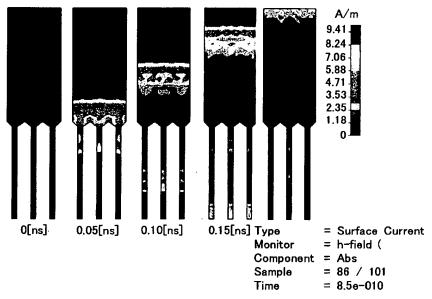


FIG.10B



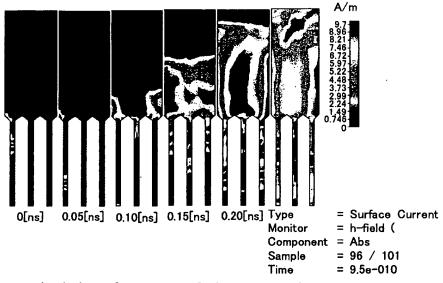
simulation of progress of electromagnetic wave around confluence of power-supply/ground pair transmission lines

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FIG.11



simulation of progress of electromagnetic wave around confluence of power-supply/ground pair transmission lines

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FIG.12A

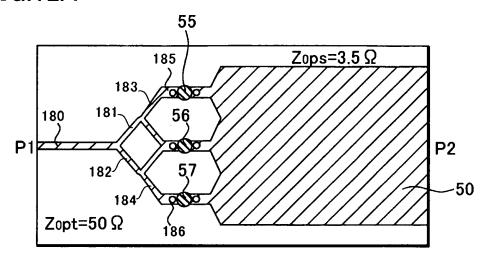
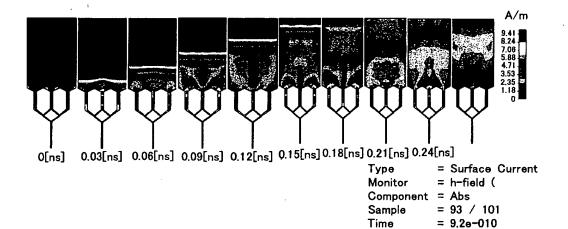


FIG.12B



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FIG.13

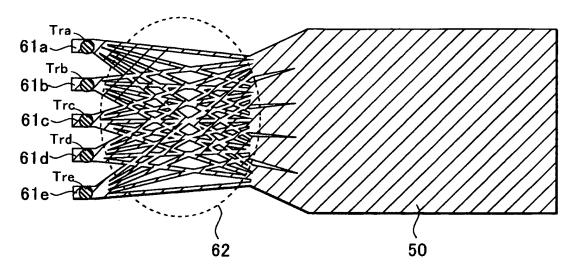
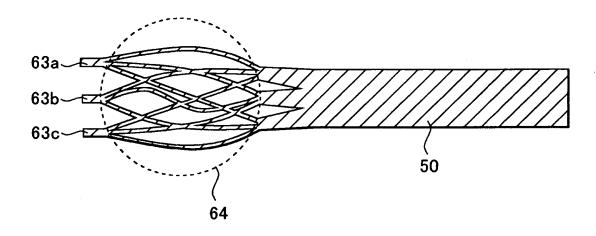


FIG.14



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FIG.15

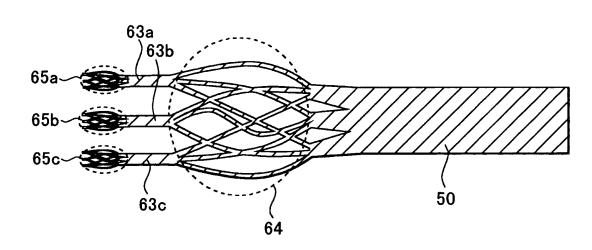
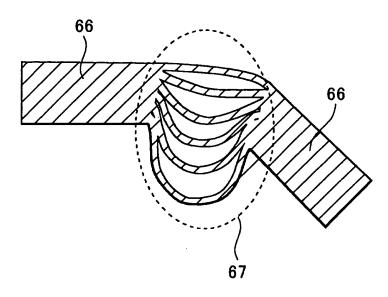
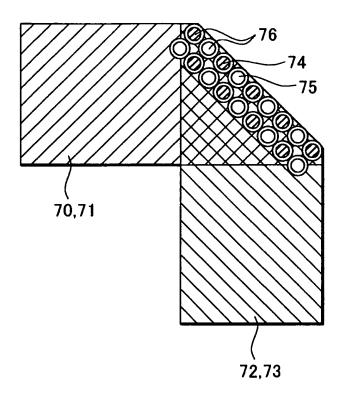


FIG.16



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FIG.17



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FIG.18A

FIG.18B

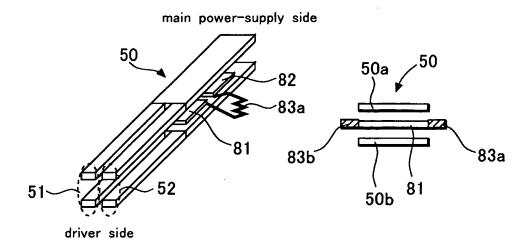
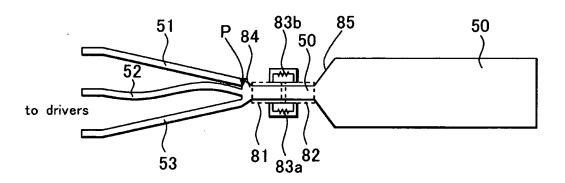


FIG.19



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FIG.20A

FIG.20B

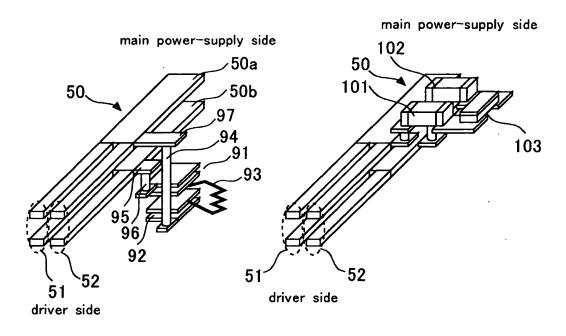
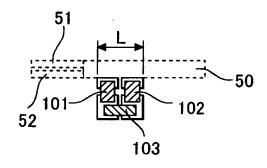


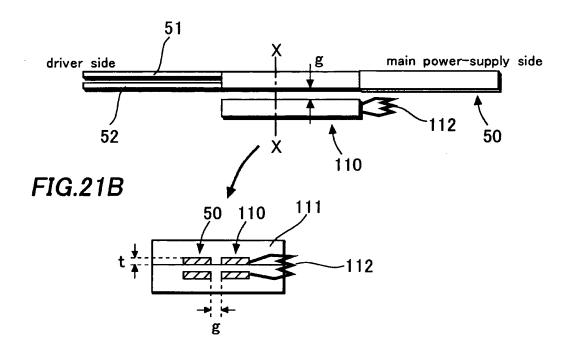
FIG.20C



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FIG.21A



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FIG.22

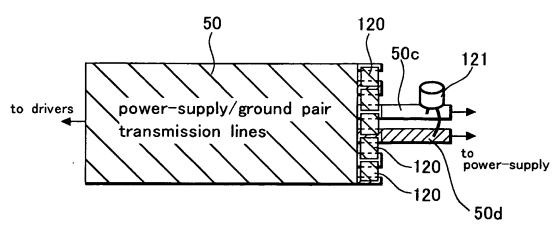


FIG.23A

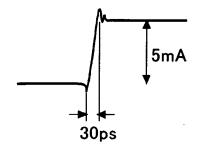


FIG.23B 5mA $30ps \times 10$ times or more

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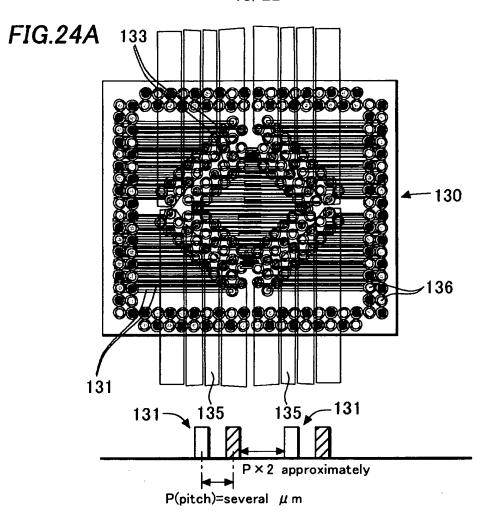
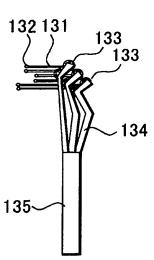


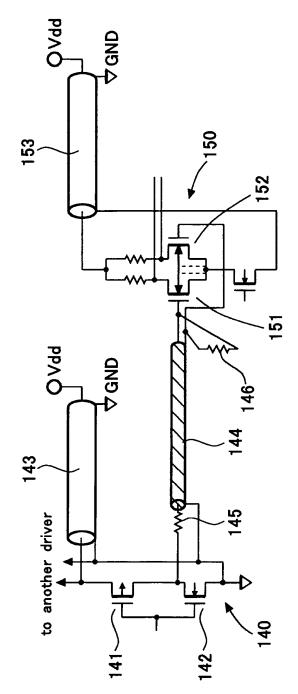
FIG.24B



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FIG.25



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FIG.26

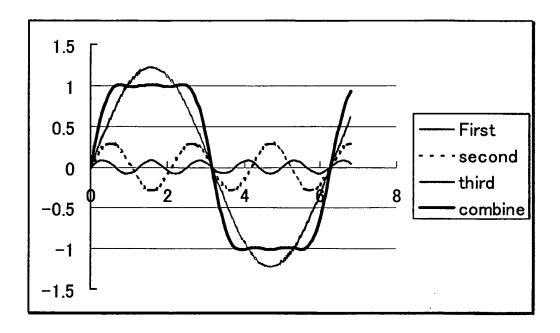
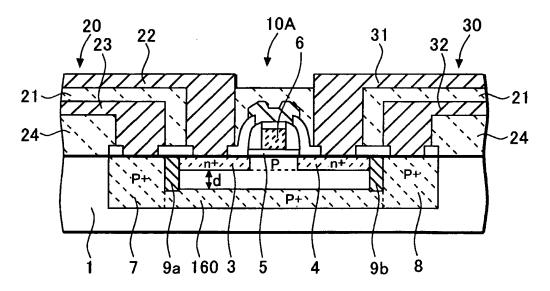


FIG.27



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FIG.28

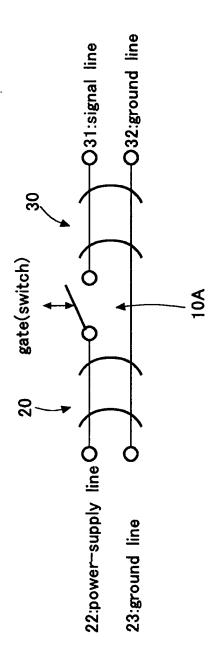


FIG.29

